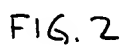
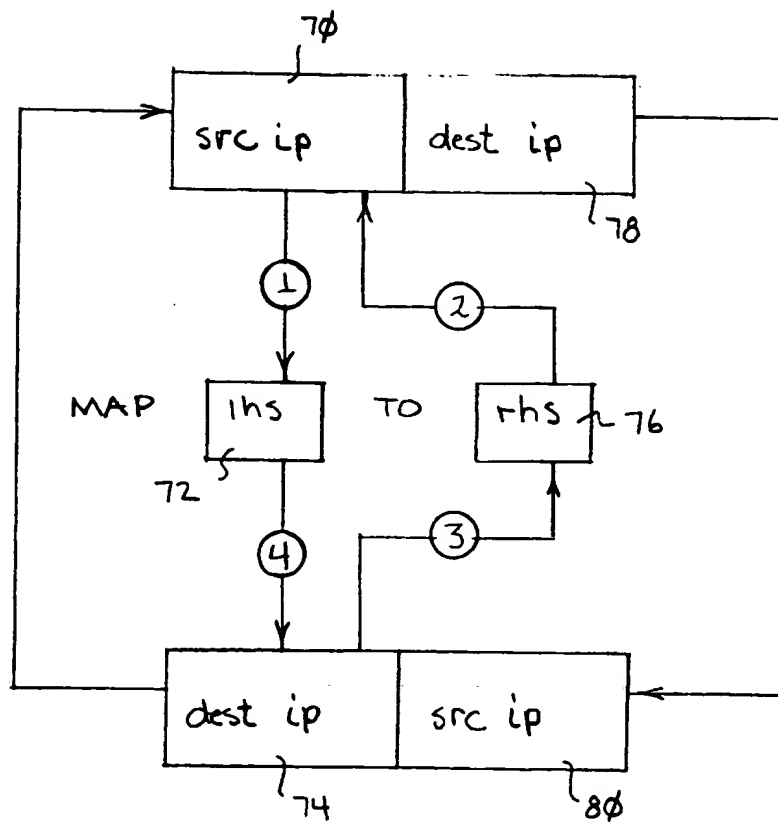
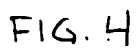


FIG. 1







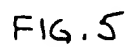




Diagram illustrating the second stage of the IP address translation process:

- Implicit Address:** The output from the first stage is an implicit address (188).
- MAP Block:** The implicit address is processed by a MAP block, which outputs an explicit address (190).
- Local Binding Table:** A table used for mapping implicit addresses to explicit ones. It contains columns for lhs, rhs, and time stamp. The table is updated with the mapping (188 to 190) and a time stamp (214).
- Explicit Address:** The output of the MAP block is an explicit address (190).
- Second Stage:** The explicit address (190) is used to look up the mapping in the Local Binding Table. The table returns the explicit address (190) and the time stamp (214).
- Final Output:** The explicit address (190) is the final output of the second stage.

FIG. 7

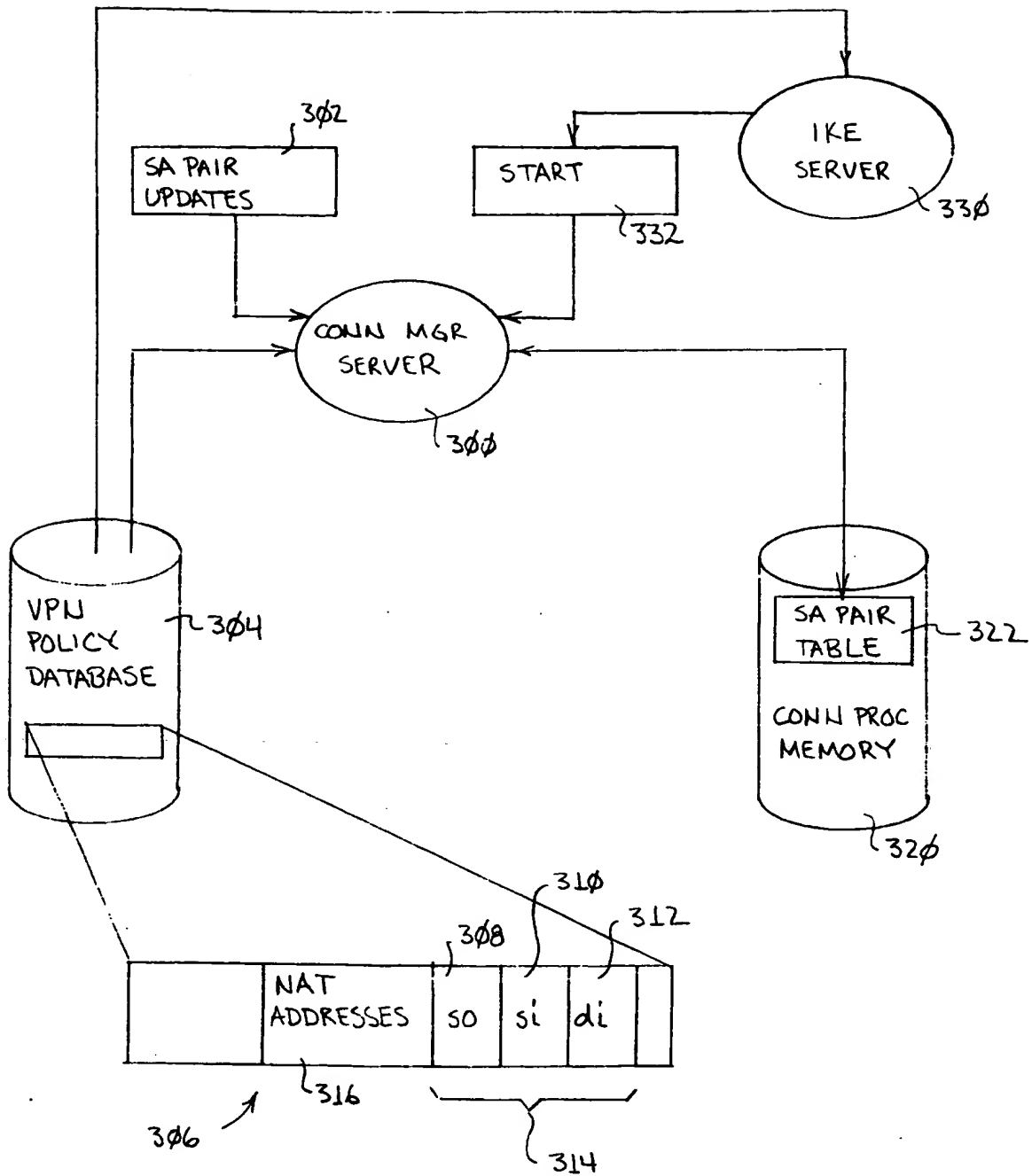


FIG. 8